

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jun Koyama, et al.

Art Unit : Examiner: 2871 H. Ngo

Serial No.: 08/770,792

: December 19, 1996

Filed Title

: A SEMICONDUCTOR DEVICE HAVING A NONCONDUCTIVE MATER

OR A WEAKLY CONDUCTIVE MATERIAL APPLIED TO A SIDE E

OF A SUBSTRATE AND A METHOD OF FABRICATING THE SAME

BOX RCE

Commissioner for Patents Washington, D.C. 20231

August 6,/2001 [Note August 4, 2001 was a Saturday]

AMENDMENT

Please amend the above-identified application as follows.

In the Specification:

Please replace the paragraph beginning at page 3, line 7 with the following rewritten paragraph:

--However, in the second-mentioned conventional structure, in an attempt to minimize the size of the liquid crystal display, the counter substrate and the TFT substrate are preferably cut along common planes (in Fig. 4, the top end surface, bottom end surface, and right end surface of each substrate) from which no terminals are brought out. Accordingly,

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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